

FACULTY VITAE

Name – Janaki Rani M

Education –

Ph.D., Low Power VLSI Design, Sathyabama University, Chennai, 2013.

M.E, Electronics, M.I.T, Anna University, 2003.

B.E, Electronics & Instrumentation, Annamalai University, Chidambaram, 1990.

Academic experience –

Dr. M. G. R Educational and Research Institute, Professor of Electronics and Communication Engineering, 2008 – Present

Sathyabama University, Assistant Professor, ECE, 1995-2008

Thiru Seven Hills Polytechnic, Lecturer, ECE, 1994-1995

S. S. Polytechnic, Lecturer, ECE, 1990-1994

Non-academic experience – NIL

Certifications or professional registrations-

NPTEL Certification on “Digital Circuits” in 2019.

Current membership in professional organizations – IAENG (International Association of Engineers)

Honors and awards- NIL

Service activities (within and outside of the institution)

Question Paper Setter / Evaluator in Sathyabama University, SRM University and Bharath University (2000-2008)

DC member in Sathyabama University & Anna University (2019)

Co-guide in Annamalai University (2018)

Ph. D Guide in Dr. M.G.R. Educational & Research Institute (2017)

Dept. Research Coordinator

BOS (Board of Studies) Member

Department Library In-charge

Briefly list the most important publications and presentations from the past five years –

Anand Kumar., Mathi Janaki Rani., M Anand., Sudeep Sharma., Chettiyar Vani Vivekanand., Ankit Chakravarti (2022),” Comparative Performance Study of Difference Differential Amplifier Using 7nm and 14nm FinFET Technologies and Carbon Nano tube FET”, Journal of Nanomaterials, Vol.2022, pp1-15.

Rajalakshmi. R., Mathi Janakirani., Margabandu Anand (2021),”A contemporary survey on low power, high speed comparators for bio-medical applications”, Advances in Parallel Computing, Vol38, pp511-516.

Jeneetha Jebanazer J., Janakirani M.(2021) “Enhanced energy harvesting system for implantable bio-medical devices with fault tolerance”, Journal of Green Engineering, Vol. 11, no. 1, pp.746-767.

Sathya S., Anand M., Janaki Rani M., Senthil Murugan J.(2019)”RF MEMS design and fabrication process for communication”, International Journal of Innovative Technology and Exploring Engineering, Vol. 8, pp.616-624 ISSN 2278-3075

Anandhi S., Neela R., Janaki Rani M. (2020), “Power optimization by using reconfigurable LFSR with gated clock”, IOP Conference Series: Materials Science and Engineering, Vol 993, no.1, pp 1-17, ISSN 1757-8981.

T Suguna., M Janaki Rani (2020), "Analysis of Adiabatic Hybrid Full Adder and 32-Bit Adders for Portable Mobile Applications", International Journal of Interactive Mobile Technologies, Vol.14, no.5, pp.73-94, ISSN-1865-7923.

G ThiraviyaSuyambu., M Anand., M Janaki Rani (2020) , “Blockchain “ A Most Disruptive Technology On The Spotlight Of World Engineering Education Paradigm”,Procedia computerscience,Vol172,pp152-158.

Neela R., Anandhi S., M J Rani (2020), "Vlsi design of area efficient test data compression architecture for IoT devices", International Journal of Advanced Science and Technology, Vol. 29, no. 4, pp.628-639

Saritha M., Janaki Rani M., Anand M. (2020), "A 45nm CMOS power amplifier with reconfigurable gain and improved linearity", Journal of Advanced Research in Dynamical and Control Systems, Vol. 12, no. 6, pp.387-396

K Manivannan., L Lakshminarasimman., M Janaki Rani (2020), "Design and Implementation of Reflected Binary Code Carry Select Adder Based Further Desensitized Half band FIR Filter", Test Engineering and Management, Vol. 82, pp.10311-10318.

Lelin Kumar M., M Janaki Rani (2019),"A Design of Novel Hybrid Opto-acoustic Modem for Underwater Communication", International Journal of Innovative Technology and Exploring Engineering, Vol.8, no.8, pp.3383-3389-3075)

Lenin Kumar M., M Janaki Rani., M Anand (2020), "Underwater optical and acoustic communication through a novel hybrid opto-acoustic modem", Journal of Advanced Research in Dynamical and Control Systems, Vol.12, no.6, pp.1723-1732(ISSN1943-023X)

T Suguna., M Janaki Rani., M Anand (2020),"Efficient charge recovery adiabatic logic design and implementation of a novel 32-bit Vedic multiplier for dsp applications", Journal of Advanced Research in Dynamical and Control Systems, Vol. 12, no. 6, pp.1709-1722.

J Jeneetha Jebanazer., M Janaki Rani., M Anand (2020), "Energy Efficient and Fault Tolerant DC-DC Boost Converter with Minimal Leakage Power for Implantable Bio-Medical ", Talent Development and Excellence, Vol. 212, no., pp.1457-1466 ISSN 1869-0459.

Briefly list the most recent professional development activities-

Online Faculty Development Programme on “Designing chips with low power artificial intelligence” from 22.04.2024 to 01.05.2024, Dr. M. G. R. Educational and Research Institute in association with NIT Warangal.

International Scientific Conference” University Science: A look into the Future” from 08.02.2024 & 09.02.2024, Kursk State Medical University, Russia cohosted by Dr. M. G. R Educational & Research Institute, Chennai.

National Workshop on Optical Wireless Technologies and its Applications in Urban and Rural Environment on 16.05.2023, NITTTR, Chennai.

Faculty Development Programme on Recent Trends in Modern Smart Intelligent Systems from 8.05.2023 to 15.05.2023, Dr. M. G. R Educational & Research Institute, Chennai.

Virtual FDP on “VLSI Design” from 18.12.2023 to 23.12.2023 Dr. M. G. R. Educational & Research Institute

Faculty Development Programme on Communication and Computation from 13.12.2022 to 17.12.2022 & 19.12.2022. Dr. M. G. R. Educational & Research Institute, Chennai.