

B.Tech (CSE)

2013,2017,2018

BOS



C. B. Palanivelu

REGISTRAR
Dr. M.G.R.
EDUCATIONAL AND RESEARCH INSTITUTE
(Deemed to be University)
Periyar E.V.R. High Road,
Maduravoyal, Chennai 600 095

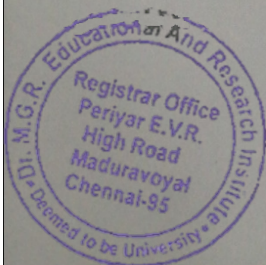
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**Dr.M.G.R
EDUCATIONAL AND RESEARCH INSTITUTE
UNIVERSITY**

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Members present for B.Tech CSE Board of Studies held on 14.02.2013

S.NO	MEMBERS NAME	ROLE	DESIGNATION	SIGNATURE
1.	Dr.V.Cyiril Raj	Chairman	HOD-CSE&IT	
2.	Mr.Suresh Durairajan	External Industry Member	Lead Architect, , Private Cloud Solutions, IBM India Pvt Ltd	
3.	Dr.P.Yogesh	External Academic Member	Associate Professor, Anna University	
4.	Dr.S.Ravi	Inter Departmental Representative	HOD-ECE	
5.	Dr.S.Tamilarasi	Internal Member	Professor	
6.	Dr.T.V.Ananthan	Internal Member	Professor	
7.	Dr.V.N.Rajavarman	Internal Member	Professor	
8.	Mrs.Sumathy Eswaran	Internal Member	Asst. Professor	
9.	Mrs. Victo Sudha George	Internal Member	Asst. Professor	
10.	Mr.Saishanmuga Raja	Internal Member	Asst. Professor	
11.	Mr.Saravanan	Internal Member	Asst. Professor	
12.	Mr.Syed Ali	Internal Member	Asst. Professor	
13.	Mrs.Shobarani	Internal Member	Asst. Professor	
14.	Mrs.Almas Begum	Internal Member	Asst. Professor	
15.	Mrs.Kavitha	Internal Member	Asst. Professor	



C. B. Palaniappan
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Chairman
14/2/13

The Board was presented with the Curriculum and Syllabus of B.Tech (CSE) applicable for B.Tech 2013

Minutes of the Meeting

The Board accepted the structure of the curriculum and also agreed upon the electives being offered as common subjects for both B.Tech CSE and B.Tech IT. The Board also agreed on the mathematics papers suggested by MATHS Board for B.Tech CSE&IT. Further the following Suggestions were given by the experts present at the Board of Studies Meeting for the curriculum and Syllabus of B.Tech CSE Full Time.

III Semester

1. In Electrical and Electronic Devices – Electrical Engg might be taught first and Electronics may be taught later. The content is sufficient for B.Tech CSE.

IV Semester

2. For “Microprocessor and Its application”, club 4th and 5th Unit. Include some more topics from Micro controller. Change the Subject name as “Microprocessor and Micro controller”.
3. Micro Processor Lab – 8086 Programs to be included. Some Micro controller programs may be included.
4. In Operating Systems Lab – Change of Exercise Names 1. Implementation of Scheduling Algorithms. Exercises 5 and 6 May be combined.
5. Career and Confidence Building may be moved to V semester.

V Semester

6. MatLab for Engineers – Change the title as “Modelling and Simulation for Engineers”. Include some Open source tools in the Lab. MatLab may be shifted in place of Computer Networks in VI semester.
7. Mat Lab – Lab may be shifted in place of “Network Programming Lab” in VI semester.
8. In OOAD, include “Applying UML and Patterns” Greig Larman’s book as a reference.
9. Remove IBM Rational Rose from the title of “Software Development Lab with IBM Rational Rose”.
10. Quantitative / Qualitative skills May be moved to VI Semester.

VI Semester

11. Computer Networks Moved to V semester in place of Matlab.
12. Network Programming Lab Move to V semester in place of Matlab Lab.
13. Include “How to Program Web” as a reference book in Web Technology.

VII Semester

14. Technology Management topics may be included in “Principles of Management”.

Electives

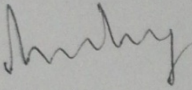
15. In Mobile and Wireless Networks, GPRS should be included.
16. Ad-Hoc Networks – Title may be changed as “Infrastructure less Networks”. Mesh Networks may be included as a separate Unit. Unit II may be removed and QOS may be added as a topic in any Unit.
17. 3G- Network – Unit II may be removed. 4G may be included. Seamless integration of Wireless Networks may be added as a unit.
18. Fault Tolerant Systems – Unit V may be removed. Disaster Recovery should be included. H/W, N/W, Storage tolerance may be included.
19. Numerical & Statistical Methods may be removed from the specialization. Performance Optimization Techniques may be included.
20. Advance Computer Architecture – Include RISC architecture in a separate unit. Merge I and II unit.
21. Name of the Research Specialization be changed as “AI Specialization”.
22. Title of “Natural language Processing” may be changed as “Natural Language Computing”.
23. “Fuzzy set theory” may be replaced with “Knowledge Engineering”

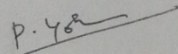
General Suggestions:

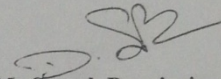
1. Lab exercises –instead of giving question, provide the concept to be practised.
2. The Board ratifies (i) Discrete Mathematics, (ii) Probability and Linear Programming and (iii) Numerical methods and advanced Linear Programming syllabus suggested by Mathematics department
3. Give appropriate titles to all the Units.

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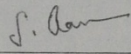
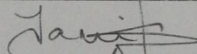
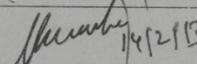
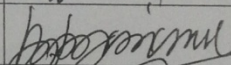
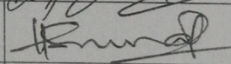




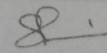


The above suggestions can be implemented in the new curriculum of B.Tech CSE


Dr.V.Cyirilraj
Chairman


Dr.P.Yogesh
External Academic Member


Mr.Suresh Durairajan
External Industry Member

Internal Members,

1.	Dr.S.Ravi	HOD-ECE	
2.	Dr.S.Tamilarasi	Professor	
3.	Dr.T.V.Ananthan	Professor	 14/2/13
4.	Dr.V.N.Rajavarman	Professor	
5.	Mrs.Sumathy Eswaran	Asst. Professor	
6.	Mrs.Victo Sudha George	Asst. Professor	
7.	Mr.Saishanmuga Raja	Asst. Professor	
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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING /
INFORMATION TECHNOLOGY

Sub: BOS Meeting for B.Tech (CSE, IT) Programs on 8th June 2017

Venue: 2nd Floor, Anna Block, Meeting Room

Agenda 1: Discussion and Ratification of B.Tech – Full Time (Computer Science Engineering)

Agenda 2: Discussion and Ratification of B.Tech (Information Technology)

Agenda 3: Discussion and Ratification of B.Tech - Part Time (Computer Science Engineering)

Agenda 4: Discussion and Ratification of Programming Lab using C for B.Tech I Year.

The following Members are present:

Dr. Sumathy Eswaran
Chairperson

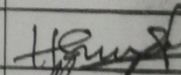
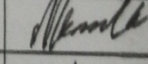
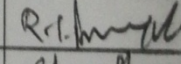
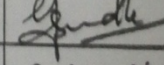
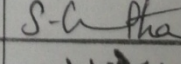
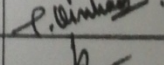
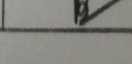
Dr. P. Yogesh
External Expert Member – Academic

Dr.N. Sadagopan
External Expert Member - Alumni / Academic

6.5h
Mr.V.S. Raghunathan
External Expert Member – Government

Mr.A.Muruganantham
External Expert Member – Industry

Internal Members:

Sl.No	Name	Designation	Functional Designation	Signature
1	Dr.Sumathy Eswaran	Professor & Head	Chairperson	
2	Dr. T.V. Ananthan	Professor	Member	
3	Dr. R.S. Ponmagal	Professor	Member	
4	Dr.G.Victo Sudha George	Associate Professor	Member	
5	Mrs. S.Geetha	Associate Professor	Member	
6	Mrs.T. Kirubadevi	Assistant Professor	Member	
7	Mrs.Kavitha.P	Assistant Professor	Member	

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Minutes of the Board of Studies meeting held on 08.06.2017

Regulations 2017 for B.TECH (Computer Science and Engineering)

At the outset the curriculum and syllabus is observed to be well done and appreciated.

The following were the suggestions recorded by the panel members present at the BOS meeting held on 08.06.17. The core papers and elective papers are analyzed meticulously and interchanged accordingly.

- The Head of Department (Dr.Sumathy Eswaran) initiated with detailing the syllabi to the external panel members.
- Dr .Yogesh (Anna University) clarified on the restrictions applied for Technical skills.
- Mr.Raghunathan (National Informatics Centre) insisted on the evaluation methods of Technical skill certification by the students, also recommended the duration of Inplant training could be atleast 3 months.He also appreciated the classification of Electives according to SMAC(social,Mobile,Analytics and cloud).Also added a point to include Content Management system and Secured Programming in the curriculum.
- Dr .Yogesh (Anna University) suggested title for the elective Fundamental of Memos to IOT or Building IOT and renaming of Perl,Python,Ruby under common frame work.
- Dr.N.Sadagopan(Alumni/IIT) spotted his suggestions from the syllabus of Data structures and algorithms .He claimed to include Algorithmic analysis and Asymptotic notations in the syllabus and to exclude 5 th unit Design Methods for UG level as it will take 20 hours exclusively.
- Mr.Muruganatham insisted upon Java Programming being taught early. Also he suggested to combine the course Object Oriented analysis and Design with Software Engineering . Hence a course and syllabus for Object Oriented Software Engineering is framed.Further he suggested to include Enterprise Architecture and Machine Learning.

The Final Changes arrived in the curriculum as below:

- No changes in semester I and II
- The course Data Structures and Algorithms could be divided into **Data Structures** in III Semester & **Design and Analysis of Algorithm** in IV Semester with respective laboratories as suggested by Dr.Sadagopan
- **Java Programming** swapped to IV semester instead of III Semester
- Computer Graphics swapped to III semester to IV semester. Since the syllabus is too heavy , it is trimmed.
- Operating Systems lab is shifted to VI semester
- Object Oriented analysis and design with Software Engineering concepts combined together as single subject as Object Oriented Software Engineering and shifted from semester V to Semester VI
- Text book recommended for Computer Networks as Peterson & Davie understandable at U.G level.
- Replace Perl, Python and Ruby as Scripting Languages
- Introducing Web Services theory and lab as part of IT Inter disciplinary subject in place of Modeling and Simulation of SciLab Theory and model.
- Included Open NMS in Network Programming lab as part of suggestion from Mr.Raghunathan (NIC).

In Electives

- Open Elective:

Artificial Intelligence excluded from the list.

- VII semester Electives

Software Testing and **Secured Programming** included .

- VIII semester Electives

Subjects like **Content Management System, Enterprise Architecture, Machine Learning** were included.

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TECHNOLOGY

The above suggestions can be implemented in the new curriculum 2017 Regulation and syllabus of B.Tech (CSE, IT)

Dr. Sumathy Eswaran
 Chairperson

[Handwritten Signature]
 2.6.17

Dr. P. Yogesh
 External Expert Member – Academic

[Handwritten Signature]

Dr.N. Sadagopan
 External Expert Member - Alumni / Academic

N. Sadagopan 8 June

Mr.V.S. Raghunathan
 External Expert Member – Government

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Mr.A.Muruganantham
 External Expert Member – Industry

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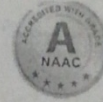
Internal Members:

Sl.No	Name	Designation	Functional Designation	Signature
1	Dr.Sumathy Eswaran	Professor & Head	Chairperson	<i>[Handwritten Signature]</i>
2	Dr. T.V. Ananthan	Professor	Member	<i>[Handwritten Signature]</i>
3	Dr. R.S. Ponmagal	Professor	Member	<i>[Handwritten Signature]</i>
4	Dr.G.Victo Sudha George	Associate Professor	Member	<i>[Handwritten Signature]</i>
5	Mrs. S.Geetha	Associate Professor	Member	<i>[Handwritten Signature]</i>
6	Mrs.T. Kirubadevi	Assistant Professor	Member	<i>[Handwritten Signature]</i>
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Department of Computer Science and Engineering / Information Technology

Board of Studies Minutes

The Board of Studies Meeting for B.Tech (CSE) was held on 19.4.2018. There is no change in the syllabus, only the curriculum has been redefined as per AICTE Guidelines.

The following points were discussed for redefining the curriculum

- Regulation 2018 is framed on the basis of AICTE Regulation 2018, in which the major revision is done on the number of credits to be earned for the award of B.Tech degree. The new revision proposes 160 credits. Based on the AICTE / our Framework the required subjects to be finalized.
- The flow of the core papers and the structure of the curriculum is satisfactory
- The first year (Semester 1 & 2) subjects remain common for all engineering disciplines.
- Soft skills 1 & 2 offered in Semester 4 & 6.
- Technical Skills 1, 2 & 3 (evaluation) offered in Semester IV, V and VI respectively.
- Suggested to offer Management paper "Management concepts and Organizational Behavior" in Semester VII and E-IV in VIII semester.
- The total credits for Project (Phase I & Phase II) is 10 as per the guidelines.
- ECE & IT subjects are considered as Interdisciplinary Theory and Lab courses.
- Suggested to have ETL papers Computer Graphics in Vth and PHP/MySQL in VIth Semesters respectively
- The Electives are common between CSE and IT unless otherwise specified as core paper
- An Open Elective I in V Sem and Open Elective II in VI sem (Interdisciplinary subject without any prerequisite) is mandated and the subjects offered are finalized for this as
- Open Elective I
 - 1 Web Design
 - 2 Cyber Security Essentials
 - 3 Software Testing
 - 4 Digital Marketing
 - 5 Python Programming



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