M. Tech. Applied Electronics – Part Time Curriculum and Syllabus- 2007

		Curriculum and Syna	1DU3- 2007			
Semester – I						
Subject code	Subject		L	T	P	C
MMA101	*Applied Engineering Mathema	tics	3	1		4
MEC103	*Digital Signal Processing		3	0		3
MEC105	*Advanced Digital System Desi	gn	3	0		3
					Total	1 10
	Code	Equivalent of the previou	ıs years / Full-	-Time / Oth	er departme	ents
	MMA101	MMA101				
	MEC103	MEC102 / MEC123				
	MEC105	MEC105				
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Semester – II						
Subject code	Subject		L	T	P	C
MEC104	*Computer Communication		3	0		3
MEC106	*VLSI design		3	0		3
MEC108	*Embedded Systems		3	0		3
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	Code	Equivalent of the previou	ıs years / Full-	Time / Oth	er departme	ents
	MEC104	MEC104	, , , , , , , , , , , , , , , , , , ,			
	MEC106	MEC106				
	MEC108	MEC110				_
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Semester – III						
	Subject		L	Т	P	C
Subject code MEC201		ations	L 3		P	C 2
MEC201 MEC203	Neural Networks and its applica *Design and Analysis of IC's	ations	3	0		3
	*Design and Analysis of IC's *Elective – I					
MECEXX			3	0		3
MEC205	*Electronic Design Laboratory		0	0	3	2
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	Code	Equivalent of the previou	is years / Full	-Time / Oth	er departme	ents
	MEC201	MEC202, MEC225				
	MEC203	MEC103				
	MECEXX					
	MEC205	MEC107, MEC 207				
Semester – IV						
Subject code	Subject		L	T	P	C
MEC202	*Advanced Digital Signal Proce	ssing	3	0		3
MEC204	*VLSI Signal Processing	2	3	0		3
MECEXX	*Elective – II		3	0		3
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	Code	Equivalent of the previou	ıs vears / Full-	Time / Oth		
	MEC202	MEC112, MEC 212	is jours / I uii	Time, ou	or departing	,1110
	MEC204	MEC121, MEC 224				_
	MECEXX	WIEC121, WIEC 224				
Semester – V	MECEAA					
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Subject code	Subject		Y			C
MMG331 MEC301			L	T	P	
DOLEST STILL	Engineering Management		3	0		3
	*Digital Control Engineering		3 3	0		3
MEC303	*Digital Control Engineering *Project Phase – I		3	0 0 	 24	3 8
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing		3 3 3	0 0 0	 24 	3 8 3
MEC303	*Digital Control Engineering *Project Phase – I		3	0 0 	 24	3 8
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing		3 3 3	0 0 0	24 	3 8 3 2
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper		3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing	Equivalent of the previou	3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper	Equivalent of the previou MMG131	3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper		3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper Code MMG331 MEC301	MMG131 MEC201	3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper Code MMG331 MEC301 MEC303	MMG131 MEC201 MEC203	3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper Code MMG331 MEC301 MEC303 MEC305	MMG131 MEC201 MEC203 MEC111, MEC311	3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper Code MMG331 MEC301 MEC303	MMG131 MEC201 MEC203	3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305 MEC307	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper Code MMG331 MEC301 MEC303 MEC305	MMG131 MEC201 MEC203 MEC111, MEC311	3 3	0 0 0	 24 	3 8 3 2 tal 19
MEC303 MEC305 MEC307 Semester – VI	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper Code MMG331 MEC301 MEC303 MEC305 MEC307	MMG131 MEC201 MEC203 MEC111, MEC311	3 3 3 	0 0 0 	To	3 8 3 2
MEC303 MEC305 MEC307 Semester – VI Subject code	*Digital Control Engineering *Project Phase – I *Digital Image Processing *Term Paper Code MMG331 MEC301 MEC303 MEC305 MEC307	MMG131 MEC201 MEC203 MEC111, MEC311	3 3 1s years / Full-	0 0 0 Time / Oth	To ter department	3 8 3 2 2 ents 19
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C: Credit

L: Lecture

T: Tutorial

P: Practical

Electives

Electives for even semester:

1.	MECE50	Modern Digital Communication Techniques
2.	MECE52	Electromagnetic interference & Compatibility
3.	MECE54	Microcontroller based system Design
4.	MECE56	Microwave Devices & Components
5.	MECE58	Soft Computing
6.	MECE60	Computer Architecture and Parallel Processing

Electives for odd semester:

1. MECE51	Computer Communication and ISDN
2. MECE53	Multimedia Compression Techniques
3. MECE55	Bio-medical Instrumentation
4. MECE57	Optical communication systems & Networks
5 MECE50	Digital Signal Processing Practice

MMA101 APPLIED ENGINEERING MATHEMATICS

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This is equivalent to the Full-Time course: MMA101

RANDOM PROCESS

Random Variables - Density & Distribution Functions, Moments, Moment Generating Functions - Two - Dimensional Random Variables - Marginal & Conditional Distribution - Random Process - Stationary & Ergodic Process - Auto Correlation Cross Correlation - Properties - Power Spectral

SPECIAL FUNCTIONS 12

Series Solutions - Bessel's Equation - Bessel's Functions - Legendre's Equation - Legendre's Polynomial - Rodrigue's Formula - recurrence Relations – Generating functions & Orthogonal Property for Bessel's Function of the First Kind.

CALCULUS OF VARIATIONS

Introduction - Euler's Equations - Functional Dependent on First and Second Derivatives - Brachistochrone Problem- Functional involving two or more Independent Variables - Isoperimetic Problem - variational Methods of Solving partial Differential Equations Rayleigh - Ritz Methods -Kantorovich Methods

LINEAR INTEGRAL EQUATIONS

Different Types of Integral Equations - Fredholm & Volterra Integral Equations - Relation between Differential and Integral Equations - Green's Functions – Fredholm's Equations with Separable kernel – iterative Methods for Solving Equations of Second kind – properties of Symmetric Kernels.

UNIT – V INTRODUCTION TO FINITE ELEMENT METHOD

Total No. of Hours: 60

Introduction - FEM - functions - Base Functions - Methods - of Approximation - Rayleigh - Ritz Method - Galerkin Method - Application to One -Dimensional & Two Dimensional Problem

Books for Study and Reference:

- Hildebrand, F.B. "Methods of Applied Mathematics", PHI, New Delhi (1992)
- Elsgolt.L., "Differential Equations & Calculus of Variations", Mir Pub Moscow, (1985). Clarke.A.B., Disney, R.L, "Probability and Random Process", John Wiley, (1970).
- Narayanan.S., et.al, "Advanced Mathematics for Engineering Students", Madras (1989).
- Zienkienviez, O.C,. "Finite Element Methods in Engg. Sciences", McGraw Hill, London, (1989)
- Papoulis, "Probability, Random Variables & Stochastic Process, McGraw Hill, New York (1991)
- Petrovsky, L.G. "Lectures on Theory of Integral function", Mit Pub. Moscow (1971)
- Andrews, L.A, "Special Functions for Scientists & Engineers", McGraw Hill, New York, (1992)

DIGITAL SIGNAL PROCESSING **MEC103**

3

This is equivalent to the Full-Time course: MEC123/MEC102

DISCRETE TIME LINEAR SYSTEMS:

Discrete linear systems - Time invariance - Causality, Stability, Difference Equations, Z-Transform and Inverse Z-Transform. Transfer function of linear discrete systems - Impulse response - Recursive, Non-Recursive filters - Digital filter realization - Direct, Canonic, Cascade, Parallel and Ladder realizations.

DESIGN OF DIGITAL FILTERS:

12

Review of design techniques for analog low pass filters - Frequency transformation - Properties of IIR filters - IIR filter design - Characteristics of FIR filters with linear phase - Frequency response of linear phase FIR filters - Design of FIR filters using Window functions. Discrete Fourier Transform (DFT) - Definition - Properties - Convolution of sequences - Linear Convolution.

FFT ALGORITHMS:

Introduction to Radix-2 Fast Fourier Transform (FFT) - Properties- Decimation in Time FFT- Data shuffling and bit reversal - Decimation in Frequency FFT- Algorithms of Radix-2 FFT - Computing Inverse DFT by doing a direct DFT.

EFFECTS OF FINITE REGISTER LENGTH:

Effect of number representation on Quantization - Quantization in Sampling Analog signals - Finite Register - Length effects in realization of IIR digital filters - Finite register length effects in realization of FIR digital filters

POWER SPECTRUM ESTIMATION (Brief Ideas):

Basic principles of Estimation Theory - Estimation of Autocovariance - Periodiagram method of Power Spectral Estimation.

Total No. of Books: 45

TEXT BOOKS:

- 1. Oppenheim A.V., and Schaffer R.W., Buck .C, "Discrete-time Signal Processing", Prentice Hall India, 1999
- 2. Sanjit K. Mitra, "Digital Signal Processing" A computer Based Approach" Tata McGraw Hill, 1997

REFERENCES:

- 1. Ludemann, L.C., "Fundamentals of Digital signal processing', John Wiley, Inc., 1992
- 2. Proakis J.B & Monalakis J., "Digital Signal Processing" -Principles, Algorithm and Application Prentice Hall of India 1998.

MEC105 ADVANCED DIGITAL SYSTEM DESIGN

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This is equivalent to the Full-Time course: MEC105

ADVANCED TOPICS IN BOOLEAN ALGEBRA

9

Shannon's expansion theorem, Conseusus theorem, Octal designation, Runmeasure, INHIBIT/INCLUSION / AOI / Driver / Buffer gates, Gate expander, Reed Muller expansion, synthesis of multiple output combinational logic circuits by product map method, Design of static hazard free and dynamic hazard free logic circuits.

THRESHOLD LOGIC

Linear seperability, Unateness, Physical implementation, Dual comparability, Reduced functions, various theorems in threshold logic, Synthesis of single gate and multigate threshold Network.

SYMMETRIC FUNCTION 9

Elementary symmetric functions, partially symmetric and totally symmetric function, Quin-Mc Cluskey decompositions method, Unity ratio symmetric ratio function, Synthesis of symmetric function by contact networks

SEOUENTIAL LOGIC CIRCUITS

Mealy machine, Moore machine, Trivial / Reversible / Isomorphic sequential Machines, State diagrams, State table minimization, incompletely specified sequential machines, State assignments, Design of synchronous and asynchronous sequential logic circuits working in the fundamental mode and pulse mode, Essential hazards, Unger's theorem.

PROGRAMMABLE LOGIC DEVICES

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Basic concepts, Programming technologies, Programmable logic Element (PLE), Programmable Logic Array (PLA), Programmable Array Logic (PAL), Structure of Standard PLD's, Complex PLD's (CPLD). System Design Using PLD's – Design of combinational and sequentional circuits using PLD's, Programming PAL device using PALASM, Design of state machine using Algorithms State Machine (ASM) chart as a design tool.

Reference Books:

1. William I. Fletcher, "AN Engineering Approach to Digital Design", Prentice Hall of India, 1996.

2. James E. Palmer, David E. Perlman, Introduction to Digital Systems", Tata McGraw Hill, 1996.

3.S.Devadas, A.Ghosh and K.Keutzer, "Logic Synthesis", McGraw Hill, 1994.

4.N.N.Biswas, "Logic Design Theory", Prentice Hall of India, 1993.

MEC104 COMPUTER COMMUNICATION

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This is equivalent to the Full-Time course: MEC104

BASICS OF NETWORKS

Telephone, Computer, Cable television and Wireless network, Networking principles, Service integration, Network services and layered architecture, traffic characterization and QOS. Networks services: Network elements and network mechanisms.

PACKET SWITCHED NETWORKS

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OSI and IP models: Ethernet (IEEE 802.3): token ring (I EEE 802.5), FDDI, DQDB, frame relay: SMDS: Internetworking with SMDS, 802.11 Standards & their deployment

INTERNET AND TECP/IP NETOWRKS

9

Overview, Internet protocol: TCP/IP : performance of TCP/IP networks-circuit switched networks: SONET; DWDM, Fibre to home, DSL. Intelligent networks, CATV.

ATM AND WIRELESS NETWORKS

9

Main features – Addressing, signalling and routing: ATM head restructure – Adaption layer, management and control: BISDN; Internetworking with ATM.

OPTICAL NETWORKS AND SWITCHING

9

Optical links – WDM systems, cross – connects, optical LAN's optical paths and networks: TDS and SDS: Modular switch design Packet switching, distributed, shared, input, and output buffers.

Total No. Of Periods: 45.

Reference Books:

- 1. Jean warland and Pravin Varaiya, High, Performance communication Networks, 2nd Edition, Harcourt and Morgan Kauffman, London, 2000.
- 2. Leon Gracia, Widjaja, Communication Networks, Tata McGraw Hill, New Delhi, 2000.
- 3. Behrouz.a. Forouzan, Data communication and Networking, Tata McGraw Hill, New Delhi, 2000.

MEC108 EMBEDDED SYSTEMS

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DATE I

This is equivalent to the Full-Time course: MEC110

9

Embedded Computer systems: - Applications, software issues, memory mapped architecture, 68HCII Architecture and different addressing modes, study of Intel 8051 microcontroller architecture and Instruction set

UNIT – II

Programming of PIC Micro controllers. Architecture of PIC Micro controllers. Instruction set of PIC micro controllers. Simple assembly language and C program for PIC microcontroller

UNIT – III 9

Software Development: - Quality programming, memory allocation, self-documenting code, Abstraction, Modular Software development Device drivers, Threads Recursion

UNIT – IV

Interfacing method.

Blind cycle counting synchronization, Gadfly Synchronization, Printer Interfacing

Interrupt Synchronization:

Reentrant programming, FIFO Queue, 6811 Interrupts Polled versus vectored interrupts

Timing Generation and Measurements

MC8811 Input capture, Period measurements, Output compare, square wave generation Frequency measurements.

UNIT – V

Serial I/O devices:

RS232 Specifications, Communication protocols, MC6811 SCI ad SPI.

Parallel port Interfaces

Input switches and keyboard, out put LED, Stepper motor

Memory Interfacing

Address switching, Memory Interface, examples for MC6H16, Introduction to High speed I/O interfacing.

Total No. of Periods: 45

Text Books

1. Jonathan.W.Valvano, "Embedded Microcomputer system", Brooks/COLE Thomson learning series

2. John B Peatman "Design with PIC Microcontroller" Latest Edison.

Reference Books:

1. "Programming and customizing the Microcontroller", Myke Predko TMH.

MEC106 VLSI DESIGN

This is equivalent to the Full-Time course: MEC106

This is equivalent to the Full-Time course in MEC106

MOS Technology and Circuits

Mos Technology and VLSI, Process parameters and considerations for BJT, MOS and CMOS, Electrical properties of MOS circuits and Device modeling-MOS Circuit Design Process - MOS Layers, Stick diagram Layout diagram Propagation delays, Examples of combinational logic design, Sealing of MOS circuits, Capacitance Calculations

Digital Circuits and Systems 9

Programmable Logic Array (PLA) and Finite State Machines, Design of ALUs, Memories and Registers.

Analog VLSI and High Speed VLSI
Introduction to Analog VLSI, Realisation of Neural Networks and Switched capacitor filters, sub-micron technology and GaAs VLSI technology.

introduction to manage v.Est, rectification of rectificities and switched capacitor metals, sub-interior technology and saids v.Est technology

Hardware Description Languages
VHDL background and basic concepts, Structural specifications of hardware design organisation and parameterisation.

VI CI D. day

VLSI applied to communication circuits, Filter design, VLSI for RF circuits, VLSI architectures for reduced critical path & low power design

applied to communication credits, riner design, vest for Kr credits, vest architectures for reduced critical pain & low power

References

1. Wayne Wolf, Modern VLSI Design, 2nd Edition, Prentice Hall, 1998.

2. Douglas A. Pucknell and Kamran Eshraghian, Basic VLSI Design Systems and circuits, Prentice Hall of India Pvt Ltd., 1993.

. Randall .L.Geiger and P.E.Allen, VLSI Design Techniques for Analog and Digital Circuits, McGraw Hill International Company, 1990.

4. Amar Mukherjee, Introduction to NMOS and CMOS VLSI System Design, Prentice Hall, 1983.

MEC201 NEURAL NETWORKS AND ITS APPLICATIONS 3 0 0 3

This is equivalent to the Full-Time course: MEC225/MEC202

INTRODUCTION TO ARTIFICIAL NEURAL NETWORKS

 $Neuro-physiology-General\ Processing\ Element-ADALINE-LMS\ learning\ rule-MADALINE-perception\ Networks$

BPN AND BAM

Back Propagation Network – Updating of output and hidden layer weights – Application of BPN – Associative memory – Bi-directional Associative Memory - Hop field memory – Traveling sales man problem

SIMULATED ANNEALING AND CPN

 $Annealing, Boltzmann\ machine-Learning-Application-Counter\ Propagation\ network-Architecture-Training-Application.$

SOM AND ART

Self-organizing map – Learning algorithm – Feature map classifier – Applications – Architecture of Adaptive Resonance theory – Pattern matching in ART network. Neocognitron: Architecture of Neocognitron – Data processing and performance of architecture of Spacio – Temporal networks for speech recognition

CASE STUDY: 9

1. Implementation of BPN algorithm in a computer language

2. Application of Neural Networks for Pattern recognition, data comparison

3. Hop field networks for an nbit A/D converter

Total No. of Periods : 45
Reference Books:

1. J.A. Freeman and B.M. Skapura, "Neural Networks, Algorithms Applications and Programming Techniques", Addison-Wesley, 1990.

2. Laurence Fausett, "Fundamentals of Neural Networks: Architecture, Algorithms and Applications", Prentice Hall, 1994.

MEC203 DESIGN & ANALYSIS OF IC'S

This is equivalent to the Full-Time course: MEC203/MEC103

CIRCUIT CONFIGURATION FOR LINEAR IC

Current Sources, analysis of difference amplifiers with active load, supply and temperature independent biasing technique voltage references.

OPERATIONAL AMPLIFIERS

Analysis of Operational amplifier circuits, slew rate model and high frequency analysis, operational amplifier noise analysis

ANALOG MULTIPLIER AND PLL

Analysis of four quadrant and variable transconductance multiplier, voltage controlled oscillator, closed loop analysis of PLL

MOS ANALOG ICS

Design of MOS Operational Amplifier, MOS Power amplifier.

MOS SWITCHED CAPACITOR FILTERS

Design techniques for switched capacitor filter.

Total No. Of Periods: 45

Reference Books:

- Behazad Razavi, "Principles of Data Conversion System Design", S.Chand & Company Ltd, 2000.
- Grey and Meyar, "Analysis and Design of Analog Ics." Wiley International, 1996. 2.
- Kenneth R.Laker, Willy M.C.Sansen, William M.C.Sansen, "Design of Analog Integrated Circuits and Systems", McGraw Hill, 1994 3.
- Grey, Wolley, Brodersen, "Analog MOS Integrated Circuits", IEEE Press, 1989.

MEC205 Electronics Design laboratory

This is equivalent to the Full-Time course: MEC207/MEC107

2

- Schematic Capture and PCB layout design using EDA tools.
- System Design using Micro controllers.
- Microcontroller based timers, Stepper motor controller, LCD & CRT Interfacing 3.
- System design using 16 bit Microprocessors. 4
- SPICE simulation of electronic circuits.
- Design of Power electronic circuits SMPS, High Frequency DC/DC Converters using Power MOSFET

Total No. Of Periods: 45.

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MEC202 ADVANCED DIGITAL SIGNAL PROCESSING

This is equivalent to the Full-Time course: MEC212/MEC112

Discrete random signal processing Discrete Random Process, Expectation, Variance, Co-Variance, Scalar Product, Energy of Discrete Signal-Parseval's Theorem, Wiener Khintchine

Random Signal Processing by Linear Systems-Simulation of White Noise - Low Pass Filtering of White Noise.

Spectrum Estimation Non-Parametric Methods-Correlation Method - Co-Variance Estimator - Performance Analysis of Estimators - Unbiased, Consistent Estimators -Periodogram Estimator - Barlett Spectrum Estimation - Welch Estimation - Model based Approach - AR, MA, ARMA Signal Modeling - Parameter Estimation using Yule-Walker Method.

Relation-Power Spectral Density -Periodogram - Sample Autocorrelation-Sum Decomposition Theorem, Spectral Factorization Theorem - Discrete

Linear Estimation and Prediction

Maximum likelihood criterion-efficiency f estimator - Least mean squared error criterion - Wiener filter - Discrete Wiener Hoff equations - Recursive estimators-Kalman filter - Linear prediction, prediction error-whitening filter, inverse filter - Levinson recursion, Lattice realization, and Levinson recursion algorithm for solving Teoplitz system of equations.

Adaptive Filters

FIR adaptive filters - Newton's steepest descent method-adaptive filter based on steepest descent method - Widrow Hoff LMS adaptive algorithm -Adaptive channel equalizations - Adaptive echo chancellor - Adaptive noise cancellation - RLS adaptive filters - Exponentially weighted RLS sliding window RLS - Simplified IIR LMs adaptive filter

Multirate Digital Signal Processing

Mathematical description of change of sampling rate - Interpolation and Decimation -continuous time model - Direct digital domain approach -Decimation by an integer factor - Interpolation by an integer factor - single and multistage realization - Poly phase realization - Application to sub band coding - Wavelet transform and filter bank implementation of wavelet expansion of signals.

Total No. of Hours = 45

Textbooks:

1. Monson H.Hayes, Statistical Digital Signal Processing and Modelling, John Wiley and Sons, Inc., New York, 1996

Reference Books

1. Sopocles J.Orfanidis, Optimum Signal Processing, McGraw Hill, 1990.

2. John G.Proakis, Dimitris G.Manolais, Digital Signal Processing Prentice Hall of India, 1995.

VLSI SIGNAL PROCESSING **MEC204**

0 0 This is equivalent to the Full-Time course: MEC224/MEC121

PIPELINING AND PARALLEL PROCESSING:

3

Introduction - Pipelining of FIR Digital filters - Parallel processing - Pipelining and parallel processing for Low power.

Introduction - Definitions and Properties - Solving system of Inequalities - Retiming Techniques.

UNFOLDING:

Introduction - An algorithm for unfolding - Properties of unfolding - Critical path, unfolding and retiming - Application of unfolding.

FOLDING:

Introduction - folding Transformation - Register Minimization Techniques - Register Minimization in folded Architectures - Folding of multirate

SYSTOLIC ARCHITECTURE DESIGN:

Introduction - systolic Array Design Methodology - FIR systolic Arrays - Selection of scheduling vector - Matrix Multiplication and 2D systolic array Design - Systolic design for space representations containing Delays .

Introduction - Cook - Toom algorithm - Winogard algorithm - Iterated convolution - cyclic Convolution - Design of Fast convolution Algorithm by Inspection.

Total No. of Periods: 45

REFERENCES:

- Keshab.K. Parhi, "VLSI Digital signal processing systems -Design and Implementations" wiley Inter science, 1999.
- 2. Mohammed Ismail, Terri, Fiez, 'Analog VLSI signal and Information Processing, 1994 McGraw Hill.
- 3. Keshab K.Parhi, VLSI Digital signal processing systems Design and Implementation Wiley - Inter science, 1999.
- Kung S.Y, H.J. While house, T.Kailath, "VLSI and Modern signal processing, Prentice hall, 1985 4.
- Jose E. France, Yannis Tsividis "Design of Analog Digital VLSI circuits for Telecommunications and signal processing" -5. Prentice Hall, 1994.

MMG331 ENGINEERING MANAGEMENT

This is equivalent to the Full-Time course: MMG331/MMG131

3

Operations Management:

Operations Management - Definition and concepts - Quality circles - Suggestion scheme for Quality improvement - Total Quality Management Concept - ISO Quality Certifications and types - Quality assurance - Quality Audit - Hr, Six Sigma Concept, Quality function deployment.

Marketing:

Basic concepts of Marketing - marketing mix - new product development (Concepts & cases). Relationship marketing ,supply chain management. Product life cycle - concepts of service marketing, global marketing concepts.

Basic concepts of finance - Project finance - Long term finance - short term finance working capital management - International finance - GDR -Financial forecasting - Leverage (Operating, Financial & Combined Leverages)

Managerial Effectiveness & Leadership:

Measuring Managerial Effectiveness. - Organizational Climate - Leadership Styles Group Influences - Team building - Organizational and Managerial Efforts - Self Development - Negotiation Skills - Development of the Competitive Spirit - Knowledge Management - Fostering Creativity. (Case Study)

Entrepreneurship Development

Entrepreneurship concept - Entrepreneurship as a Career- Entrepreneur - Personality Characteristics of Successful. Entrepreneur- Knowledge and Skills Required for an Entrepreneur. Business Environment - Central and State Government Industrial Policies and Regulations Business Plan Preparation: Matching Entrepreneur with the Project - Feasibility Report Preparation and Evaluation. (Case Study)

Reference Books:

Total No. of Periods: 45

- R. Panneerselvam, "Production and Operation Management", Prentice Hall of India, 2002.
- Reddin W.J. Effective Management, Tata McGraw Hill Company, New Delhi, 1995.
- S.S.Khanka, Entrepreneurial Development, S.Chand and Company Limited, New Delhi, 2001.
- Gaither, "Production and Operations Management (with two CDs)", Thomson Asia (P) Ltd., Bombay, Ninth Edition, 2002.
- Chase, Aquilano & Jacobs "Production and Operations Management ", Tata McGraw Hill, Sm Edition, 1999. 6. Principles of Marlreting: Philip Kotler, Tata McGraw ~ 8m Edition, 2004.
- Chakraborthy, Managerial effectiveness and Quality of Work life: Indian Insights, Tata McGraw Hill, 2000.
- S. Hisrich, Entrepreneurship, Tata McGraw Hill, New Delhi, 2001.

DIGITAL IMAGE PROCESSING **MEC305**

3

This is equivalent to the Full-Time course: MEC311/MEC111

CONTINOUS AND DISCRETE IMAGES AND SYSTEMS

Light, Luminance, Brightness and Contrast, Eye, The Monochrome Vision Model, Processing Problems and Applications, Vision Camera, Digital Processing System, 2-D sampling Theory, Aliasing, Image Quantization, Lloyd Max Quantizer, Dither, Color Images, Linear Systems And Shift Invariance, Fourier Transform, Z-Transform, Matrix theory Results, Block Matrices and Kronecker Products.

IMAGE TRANSFORMS

2-D orthogonal and Unitary transforms, 1-D and 2-d DFT, Cosine, Sine, Walsh, Hadamard, Haar, Slant, Karhunen-loeve, singular value Decomposition transforms.

IMAGE ENHANCEMENT

Point operations - Contrast stretching, clipping and thresholding density slicing, Histogram equalization, modification and specification, spatial operations - Spatial averaging, low pass, high pass, band pass filtering, direction smoothing, medium filtering, generalized cepstrum and homomorphic filtering, edge enhancement using 2-D IIR and FIR filters, color image enhancement

IMAGE RESTORATION

Image observation models, sources of degradation, inverse and Wiener filtering, geometric mean filter, non-linear filters. Smoothing splines and interpolation, constrained least squares restoration.

IMAGEDATA COMPRESSION AND IMAE RECONSTRUCTION FROM PROJECTION

Image data rates, pixels coding, predictive techniques transform coding and vector DPCM, Block truncation coding, wavelet transform coding of images, color image coding. Random transform.

Total Number of Periods: 45

Reference Books:

- 1. Anil K. Jain, "Fundamentals of Digital Image Processing", PHI 1995.
- M.A.Sid Ahmed, "Image Processing", McGraw Hill, Inc, 1995.
- 3. R.Gonzalazand P.Wintz, "Digital Image Processing", Addition Wesley 2nd Ed, 1987.
- 4. William. K.Pratt, "Digital Image Processing", Wiley Interscience, 2nd Ed, 1991.

MEC301 DIGITAL CONTROL ENGINEERING

3 0

3

This is equivalent to the Full-Time course: MEC301/MEC201

PRINCIPLES OF CONTROLLERS

9

Review of frequency and time response analysis and specifications of control systems, need for controllers, continues time compensations, continues time PI, PD, PID controllers, digital PID controllers.

SIGNAL PROCESSING IN DIGITAL CONTROL

0

Sampling, time and frequency domain description, aliasing, hold operation, mathematical model of sample and hold, zero and first order hold, factors limiting the choice of sampling rate, reconstruction.

MODELLING AND ANALYSIS OF SAMPLED DATA CONTROL LANGUAGE

9

Difference equation description, Z-transform method of description, pulse transfer function, time and frequency response of discrete time control systems, stability of digital control systems, Jury's stability test, state variable concepts, first companion, second companion, Jordan canonical models, discrete state variable models, elementary principles.

DESIGN OF DIGITAL CONTROL ALGORITHMS

0

Review of principle of compensator design Z-plane specification, digital compensator design using frequency response plots, discrete integrator, discrete differentiator, development of digital PID controller, transfer function, design in the Z – Plane

PRACTICAL ASPECTS OF DIGITAL CONTROL ALGORITHMS

9

Algorithm development of PID control algorithms, software implementation, implementation using microprocessors and microcontollers, finite word length effects, choice of data acquisition systems, microcontroller based temperature control systems, microcontroller based motor speed control systems.

Total No. Of Periods: 45

Text Book:

1. M.Gopal, "Digital control and static variable Methods", Tata McGraw Hill, New Delhi, 1997.

Reference Books:

Digital Modulation

- 1. John J.D'Azzo, "Constantine Houpios, Linear Control System Analysis nad Design", McGraw Hill, 1995.
- Kenneth J. Ayala, "The 8051 Microcontroller Architecture, Programming and Application", Penram International, 2nd Edition, 1996.

MECE50 MODERN DIGITAL COMMUNICATION TECHNIQUES

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3

This is equivalent to the Full-Time course: MECE50

Functional architecture coded and encoded digital communication system architecture

0

PSD, Data pulse stream, M-ary Markov source, convolutionally coded modulation, continous phase

Coherent and Non-coherent communication waveform

9

Optical receiver in WGN, MF receiver, Matrix generation, colored GN, Whitening approach, inphase and Quadrature phase modern

Band Limited Channels

9

Optimum pulse shape design, Optimum demodulations of digital signals in the presence of ISI

Coded Digital Communication

9

Architecture, interfacing, detailing, Synchronization, block coded digital communication system, performance, type of binary block codes, Shannon channel coding theorem, linear block codes.

Total No of periods: 45

Reference Books:

- 1. Simon Haykin, Digital Communications, John Wiley and sons, 1998.
- 2. Wayne Tomasi, Advanced Electronic communication system, 4th Edition, Pearson Education Asia, 1998
- 3. B.P.Lathi, modern Digital and analog communication systems, 3rd Edition, Oxford University Press, 1998.
- 4. M.K.Simon, S.M.Hindedi and W.C.Lindsey, Digital Communication techniques: signalling and detection, Prentice Hall India, New Delhi. 1995.

MECE51 COMPUTER COMMUNICATION AND ISDN

U

This is equivalent to the Full-Time course: MECE51

9

Evolution of data Networks, Network architecture, ISO Reference model example of networks, Application of networks.

MEDIUM ACCESS SYBLAYER & DATA LINK LAYER

9

Local area networks, conventional channel allocation methods, pure – ALOHA, S-ALOHA, finite population ALOHA, Controlled ALOHA, Reservation ALOHA, Design issues for packet radio networks- IEEE standards for LAN – Ethernet.

NETWORK AND TRANSPORT LAYER

COMPUTER COMMUNICATION

9

Network layer design issues – Routing algorithms- Congestion control algorithms – Internet working. Transport layer design issues – connection management

QUEING THEORY AND CAPACITY ASSIGNMENT

9

M/M/I Queues, M/G/I – Priority queuing capacity assignment for terminal network and distributed networks – concentration and buffering for finite and infinite buffers

PRESENTATION LAYER & APPLICATION LAYER

Design issues – Abstract syntax notation – Data compression techniques – Cryptography – Remote procedure call. Design issues – file transfer access and managements.

Total No. of Periods: 45

0

3

Reference Books:

Andrew S. Tanenbaum, Computer Networks - Prentice Hall of India 1990.

D.Bertsekas and R.Gallaghery, Data Networks - Prentice hall of India, 1989.

Fred Halsall, Data Communication, Computer Networks and open system – Addition Welsey 2000. 3

Gerd E.Keiser, Load Area Networks - McGraw ill Publication, 1989.

MECE52

ELECTROMAGNETIC INTERFERENCE AND COMPATIBILITY IN SYSTEM DESIGN

This is equivalent to the Full-Time course: MECE52

EMI ENVIRONMENT

Sources of EMI, conducted and radiated EMI, Transient EMI, EMI-EMC definitions and units of parameters. Emi Coupling Principles Conducted, Radiated and Transient Coupling, Common impedance Ground Coupling, Radiated Common Mode and Ground Loop coupling, Radiated Differential Mode Coupling, Near Field Cable to cable coupling, Power mains and Power supply Coupling.

EMI SPECIFICATION / STANDARDS / LIMITS

Units of specification, Civilian standards Military standards.

EMI MEASUREMENTS

EMI Test Instruments ? Systems., EMI Test, EMI shielded Chamber, Open Area Test Site, TEM Cell Antennas, Conductors Sensors / Injectors / Couplers,. Military Test Method and Procedures, Calibration Procedures.

EMICONTROL TECHINQUES

Shielding, Filtering, Grounding, Bonding, Isolation Transformer, Transient Suppressors, Cable Routing, Signal Control, Component Selection and Mounting.

EMI DESIGN OF PCBs

PCB Traces Cross Talk, Impedance Control, Power Distribution Decoupling, Zoning Motherboard Design and Propagation Delay Performance Models. Total Number of Periods: 45.

Reference Books:

Bernhard Keiser." Principles of Electromagnetic compatibility", Artech House, 3rd Ed, 1986. 1.

Henry W.Ott, "Noise Reduction Techniques in Electronic Systems", John Wiley and Sons, New York, 1988. 2

3. V.P.Kodali, "Engineering EMC Principles, Measurements and Technologies", IEEE Press, 1996.

MULTIMEDIA COMPRESSION TECHNIQUES MECE53

This is equivalent to the Full-Time course: MECE53

Brief history of data compression applications, Overview of information theory

3

Text Compression

Compact techniques- Huffman coding - arithmetic coding - Shannon Fano Coding and dictionary techniques - LZW family algorithms. Entropy measures of performance - Quality measures.

Audio Compression

Audio Compression techniques- frequency domain filtering- basic sub band coding - application to speech coding

Image Compression

Total No of periods: 45

Predictive techniques-PCM, DPCM,DM.

Video Signal representation, Video Compression techniques-MPEG, Motion estimation techniques.

References

Mark Nelson, Data Compression book, BPB Publishers, New Delhi, 1998.

2 Savood Khaleed, Introduction to Data Compression, Morgan Kauffman, London, 1995.

3. Warkinson, J.Compression in video and audio, Facol press, London. 1995

4. jan Vozer, Video compression for multimedia, AP profes, Newyork, 1995

MECE54

Microcontroller Based System Design

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3

This is equivalent to the Full-Time course: MECE54

Intel 8051 Architecture - Hardware - I/O ports - External Memory - Counters and Timer - Serial data I/O - Interrupts, Assembly language, Addressing modes, Instruction Set - Simple programs, 8051 interfacing to LCD, ADC, DAC and Stepper Motors,

UNIT- II

IINIT - I

Motorola 68HC11 Architecture - Input / Output ports - Resets and self protection - Interrupt Timing - A/D D/A converters.

Intel 8096 CPU Structure - Register file - Assembly Language - Addressing modes - Instruction set - simple programs.

Interrupt structure - Programmable timers - Real time clock latency - Interrupt density and Interval consideration, shared resources and critical regions.

IINIT - V

Queues - Table and Strings - Program organisation - State machines - Key switch parsing - Timing consideration - UART ports - I/P O/P Serial ports programmable controllers.

Total Number of Hours: 45 Reference Books:

- Kenneth J. Ayala, "The 8051 Microcontroller Architecture, Programming & Applications" Penram International publishing (India), Second Edition, 1996.
- Muhammed Ali Mazidi, Janice Gillies Pie Mazidi, "The 8051 Microcontroller and Embedded Systems" Pearson Education Asia. 2
- PEATMAN J.B, "Design with Microcontrollers" McGraw Hill Book International Ltd, Singapore, 1989.
- Intel Manual on 16 bit "Embedded controllers", Santa Clara, 1991.

MECE55 BIOMEDICAL INSTRUMENTATION 3 This is equivalent to the Full-Time course: MECE55 UNIT - I Basic physiology-study of cells & their structures-Nervous system Basic Transducer principles & Electrodes - Bio-Chemical transducers Cardiovascular system- ECG- blood pressure measurement

X-ray & Radioisotope Instrumentation – Fluoroscopy-Image intensifiers

Bio-telemetry - Anatomy of Nervous system - EEG-EMG

Microstrip circuit design and applications

Total No. of periods: 45

Reference Books:

- M.ARUMUGAM, Bio-medical Instruemntation-Anuradha Agencies Publishers, 1992.
- LESIS CROMWELL FRED, J.WERBELL and ERICH A.PFRAFFER, Bio-medical Instruemntation and measurements- Prentice Hall of India,
- KHANDPUR, Handbook on Bio-medical Instrumentation-Tata McGraw Hill Co Ltd., 1989.

Introduction to coupled Microstrip, Even and odd mode analysis, Directional couplers, branch couplers.

MECE56 MICROWAVE DEVICES & COMPONENTS 3

This is equivalent to the Full-Time course: MECE56

Microstrips Lines, Design, Analysis Introduction, Types of MICs and their technology, propagating models, Analysis of MIC by conformal transformation

Coupled Microstrip, Directional couplers and lumped

Non-Reciprocal Components and Active Devices for MICS Ferromagnetic substrates and inserts, Micro strip circulators, Phase shifters, Microwave transistors.

Introduction, Impedance transformers, Filters, High power circuits, Low power circuits.

MMIC Technology

Fabrication process of MMIC, hybrid MICS, configuration, Dielectric substances, thick and thin film technology, Testing methods, Encapsulation and mounting of Devices.

Total No of Periods: 45

Reference

- Hoffman R.K "Handbook of microwave intergrated circuits", Artech House, Bostonm, 1987.
- Gupta. K.C and Amarjit Singh, "Microwave Intergrated circuits" John Wiley, New York, 1975.

MECE57 OPTICAL COMMUNICATION SYSTEMS AND NETWORKS 3

This is equivalent to the Full-Time course: MECE57

FIBRE OPTIC GUIDES

Light wave generation systems, systems components, optical fibers, SI, GI fibre, modes, Dispersion in fibers, Limitations due to dispersions, fibre loss, non liner effects

OPTICAL TRANSMITTERS AND FIBRES

Basic concepts, LED structures spectral distribution, semiconductor lasers, gain coefficients, modes, SLM and STM operation, Transmitter design, Receiver PIN and APD diodes design, noise sensitivity degradation,

LIGHT WAVE SYSTEM

Coherent, homodyne and Hetro dyne keying formats, BER in synchronous and Asynchronous.

Basic concepts, Semiconductor laser amplifiers Raman-and Brillouin-fibre amplifiers, Erbium doped-fibre and amplifiers, pumping phenomenon

DISPERSION COMPENSATION

Limitations, post-and pre-compensation techniques, equalizing filters Total No. of periods: 45

Reference Books:

- 1. Franze & Jain, Optical communication, systems and components, Narosa Publication, new Delhi, 2000
- 2. G.Keiser, Optical fibre communication, system, McGraw Hill, Newyork, 2000
- 3. G.P Agarwal, Fibre optic communication system. 2nd Edition, John Wiley &b Sons, New York, 1997.
- 4. Franz and Jain, Optical communication system, Narosa Publications, New Delhi, 1995

MECE58 SOFT COMPUTING 3 0

This is equivalent to the Full-Time course : MECE58
ARTIFICIAL NEURALS

Basic-concepts- single layer perception- Multi layer perception - Supervised and un supervised learning back propagation networks, Application

FUZZY SYSTEMS

Fuzzy sets and Fuzzy reasoning - Fuzzy matrices - Fuzzy functions-decompositions

NEURO-FUZZY MODELLING 9

Adaptive networks based Fuzzy interfaces - Classification and Representation trees- Date dustemp algorithm

GENETIC ALGORITHM 9

Survival of the fittest- Pictures computations-cross over mutation-reproduction-rank method-rank space method, application

SOFT COMPUTING AND CONVENTIONAL AI

AI Search algorithm-Predicate calculus rules of interface- Semantic networks- frames- objects- Hybrid models-applications

Total No. of periods: 45

Reference Books:

- 1. Jang J.S.R., Sun C.T and Mizutami E-Neuro Fuzzy and Soft computing Prentice Hall New Jersey, 1998.
- 2. Nih.J.Ndssen Artificial intelligence, Harcourt Asia Ltd., Singapore, 1998.
- 3. Timothy J.Ross: Fuzzy Logic Engineering Applications McGraw Hill, New York, 1997
- 4. Laurance Fauseett: Fundamentals of Neural networks, prentice Hall India, New Hersey, 1995.

MECE59 DIGITAL SIGNAL PROCESSING PRACTICE

This is equivalent to the Full-Time course: MECE59

- 1. Study of TMS320C3X Processors
- Study of TM3520C5X Frocessors
 Implementation of Linear Convolution
- 3. Implementation of circular convolution
- 4. Implementation of DFT & IDFT
- 5. Compression of Analog Signal
- 6. Interfacing with I/O deices through CODEC chip, etc.

MECE60 COMPUTER ARCHITECTURE AND PARALLEL PROCESSING 3 0 0 3

This is equivalent to the Full-Time course: MECE60

THEORY OF PARALLELISM 15

Parallel computer models – The state of computing, multiprocessors and multicomputers and Multivectors and SIMD computer, PRAM and VLSI models, and Architectural development tracks. Program and networks properties, Conditions of parallelism, Program partitioning architecture. Principles of scalable performance – Performance matrices and measures, parallel processing applications, speedup performance laws, scalability analysis and approaches.

HARDWARE TECHNOLOGIES 11

Processor and memory hierarchy advanced processor technology, super scalar and vector processor, memory hierarchy technology, virtual memory technology, bus cache and shared memory – back plane bus systems, cache memory organisations, shared memory organisations, sequential and weak consistency models.

PIPELINING AND SUPERSCLAR TECHNOLOGIES

10

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3

Parallel and scalable architectures, Multiprocessor and Mutlicomputers, Multivector and SIMD computer, scalable, Multithreaded and data flow architectures.

SOFTWARE AND PARALLEL PROGRAMMING

10

 $Parallel\ models,\ Languages\ and\ compilers,\ Parallel\ program\ development\ and\ environments,\ UNIX,\ MACH\ and\ OSF\ /\ 1\ for\ parallel\ computer.$

Total No. of Periods: 45

Reference Books:

- 1. M.J.Quinn, "Designing Efficient Algorithms for Parallel Computers", McGraw Hill International, 1994.
- 2. Kai Hwang, "Advanced Computer Architecture", McGraw Hill International, 1993.
- 3. William Stallings, "Computer Organization and Architecture", Macmillan Publishing company, 1990.